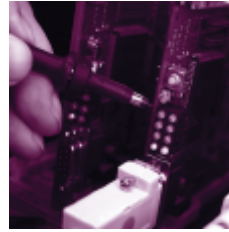


Specifications EPPE 8/R8/L8



System concept The devices of the EPPE series are high-accuracy power quality analysers which can comprehensively monitor network parameters for conformity to valid quality standards. The implementation of 32/128 bit DSP technology coupled with a high degree of component integration and powerful processors result in a sample rate of 37.5 kHz per channel. All functions and parameters are freely configurable in the software.

Measurement quantities (standard model)	Voltage	4 channels
	Current	4 channels
	Frequency	1 channel
	Phase angle	8 channels
	THD	8 channels
	Harmonics	8 channels
	Interharmonics	4 channels
	Active, apparent and reactive power	4 channels and system
	Active, apparent and reactive energy	4 channels and system
	Power factor	4 channels and system
	Symmetrical components	System
	Imbalance	System
	Impedance	4 channels and system
Flicker	4 channels	
Analog inputs	Frequency range	DC to 6 kHz, linear frequency response
	Resolution	16 bit
	Error	0.1%
	Voltage inputs	4x0 to 400 VAC, optionally to customer specifications
	Current inputs	4x0 to 2 VAC, optionally to customer specifications or 4x0 to 20 AAC, optionally to customer specifications
Protection	Galvanic isolation via opto-couplers (LOC), phase-to-phase and phase-to-earth > 2.5 kV	
Binary inputs	Trigger range	24 to 300 VDC single range
	Input current	Approx. 3 mA per channel
	Input power	Approx. 70 to 900 mW per channel
	Protection	Polarity protection and galvanic isolation via opto-couplers
Binary outputs	Sw. capacity DC	220 V, 2 A, 60 W
	Protection	Potential-free and galvanically isolated output relays
Triggers	Analog signals	Programmable limit values for over- and under-triggers and d/dt for voltages, currents, phase angle, frequency, imbalance, THD, individual harmonics, power factor, zero/pos./neg. sequence, impedance, P_{ST} , P_{LT} and other power quality values. All triggers can be activated simultaneously.
	Binary signals	Rising or falling edge at the same time for all channels

General properties	Operation, system control, data storage and analysis using a standard external or internal Windows PC.	
External analysis unit (Minimum requirements)	IBM-compatible PC, Pentium processor 200 MHz, 128 MB RAM, 5 GB HD, CD-ROM	
User interface	NRGCenter software package under Windows 2000/XP	
Power supply	Rated voltage 85 to 265 VAC, 47 to 63 Hz, 120 to 350 VDC UPS for approx. 20 minutes	
Connections	Via 4mm safety banana sockets, multi-pole system sockets or screw-type terminals located on the back panel	
Interfaces	EPPE R8	RS232, USB
	EPPE 8	RS232,USB, Ethernet or analog modem
	EPPE L8	2 x RS232, Centronics parallel, Ethernet, analog modem, USB Ext. VGA, keyboard (PS2), mouse (PS2)
Internal clock accuracy	15 ppm	
Time synchronisation	DCF, GPS, external seconds pulse	
Measuring unit memory	16 MB SDRAM, 16 MB Flash RAM	
Keyboard	Membrane keypad on the front panel or touch screen	
Display	TFT screen or illuminated LCD	
Status indication	8 Status LEDs on the front panel	
Environment	Operating temperature	0 to 50°C
	Relative humidity	5 to 90%, non-condensing
	Protection	IP20
	Safety standard	EN 61010-1 300 V~CAT II
	EMC emissions	EN 50081-2 industrial
	Susceptibility	EN 50082-2 industrial
Certification	DKD calibration certificate	

Product specifications	EPPE L8	EPPE 8	EPPE R8
Analog inputs			
Total number	8/12 ¹	8	8
Voltage inputs	4	4	4
Current inputs, switchable			
- for current clamps (up to 2 VAC)	4	4	4
- for direct current (up to 20 AAC)	4		
Binary inputs	8	8	8
Binary outputs	2	2	2
Integrated evaluation unit	■		
Integrated UPS	■	■	
Display			
TFT VGA colour screen	■		
LCD		4x20 characters	
Touch screen	■		
Keyboard		■	
Status display	Power LED	8 LEDs	8 LEDs
Housing	½ 19", 3 HU with handle	compact, portable	compact, for DIN rail
Dimensions (W x H x D) [mm]	260x165x315	160x78x260	160x63x200
Weight	2.5 kg	1.5 kg	1.0 kg

¹ EPPE L8 can be equipped for direct current measurement or for current measurement with transducers, but also for both.

² Option for direct current measurement upto 20 AAC: Therefore 4 binary inputs cease to exist.